



# LCD MODULE SPECIFICATION

**Customer:** \_\_\_\_\_  
**Model Name:** HC101IK50050-A79  
**Date:** 2019.04.23  
**Version:** 01

- Preliminary Specification  
 Final Specification

## For Customer's Acceptance

Approved by	Comment

Approved by	Reviewed by	Prepared by

**2. Revision Record**

Date	Rev.No.	Page	Revision Items	Prepared
2019.04.23	01	All	The First Release	

### 3. General Specifications

HC101IK50050-A79 is a TFT-LCD module. It is composed of a TFT-LCD panel, driver IC, FPC, a back light unit. The 10.1" display area contains 1024 x 600 pixels and can display up to 16M colors. This product accords with RoHS environmental

Item	Contents	Unit	Note
LCD Type	TFT	-	
Display color	16M		1
Viewing Direction	6	O'Clock	
Operating temperature	-20~+70	°C	
Storage temperature	-30~+80	°C	
Module size	235x143 x5.0	mm	2
Active Area(W×H)	222.72 x 125.28	mm	
Number of Dots	1024×RGB×600	dots	
Power Supply Voltage	3.3	V	
Outline Dimensions	Refer to outline drawing	-	
Backlight	33-LEDs (white)	pcs	
Data Transfer	R G B	-	

criterion.

Note 1: Color tune is slightly changed by temperature and driving voltage.

Note 2: Without FPC and Solder.

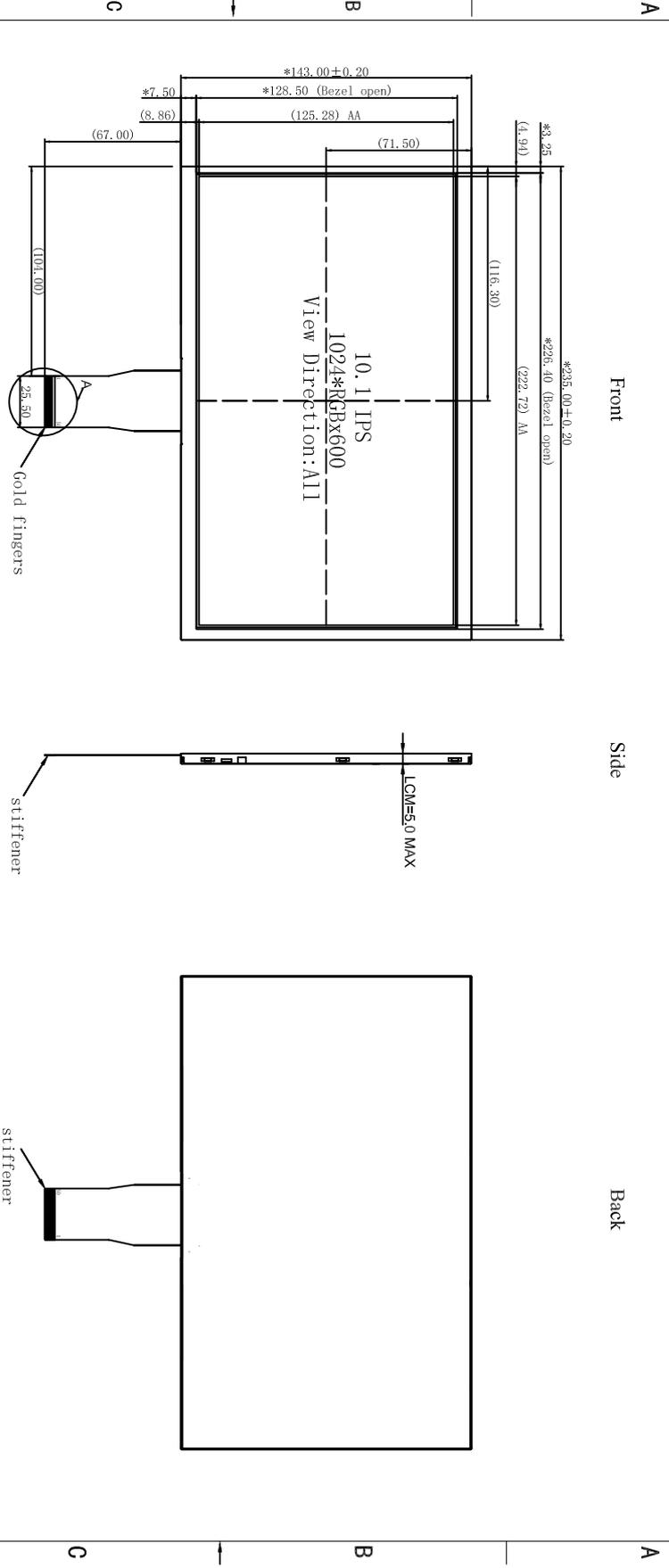
**ROHS**

REV	DESCRIPTIONS	DATE
01	NEW	2019.04.23

Front

Side

Back



- NOTES:
1. Display size:10.1"IPS 1024\*600
  2. Viewing direction:Full View
  3. Display mode:Transmissive/Normal black/Anti-glare
  4. Operation temperature:-20°C~+70°C
  5. Storage temperature:-30°C~+80°C
  6. Backlight : White(LED)/9.9(TYP)V/
  7. Unspecification tolerance are ±0.2mm

DETAIL :B

VERSION	DATE		
01	2019.04.23		
SCALE	UNIT		
1:1	mm		
APPROVED	CHECKED	DRAWN	FILE NAME:
William	Bombi	Zane	

# 4.Outline.Drawing

## 5. Absolute Maximum Ratings(Ta=25°C)

### 5.1 Electrical Absolute Maximum Ratings.(Vss=0V ,Ta=25°C)

Item	Symbol	Min.	Max.	Unit	Note
Power Supply Voltage	V <sub>CC</sub>	-0.3	3.6	V	1, 2
Logic Signal Input /Output Voltage	V <sub>I/OVCC</sub>	-0.3	V <sub>CC</sub> +0.5	V	
Power Supply Voltage for LCD	V <sub>op</sub>	0	3.6	V	

Notes:

1. If the module is above these absolute maximum ratings. It may become permanently damaged.  
Using the module within the following electrical characteristic conditions are also exceeded, the module will malfunction and cause poor reliability.
2. V<sub>CC</sub> > V<sub>SS</sub> must be maintained.
3. Please be sure users are grounded when handing LCD Module.

### 5.2 Environmental Absolute Maximum Ratings.

Item	Storage		Operating		Note
	MIN.	MAX.	MIN.	MAX.	
Ambient Temperature	-30°C	80°C	-20°C	70°C	1,2
Humidity	-	-	-	-	3

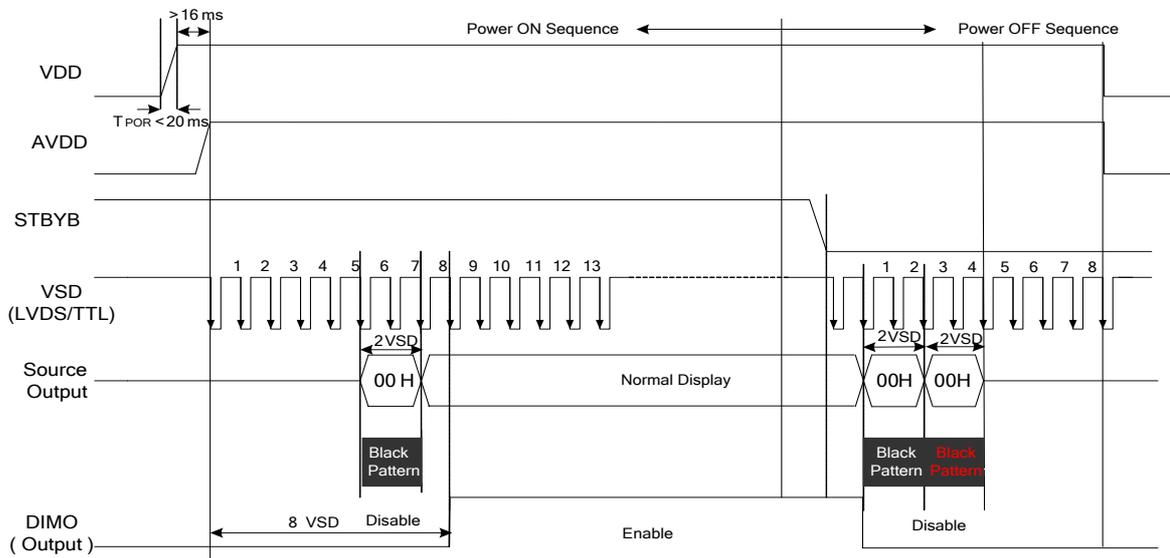
1. The response time will become lower when operated at low temperature.
2. Background color changes slightly depending on ambient temperature.  
The phenomenon is reversible.
3. Ta<=40°C:85%RH MAX.  
Ta>=40°C:Absolute humidity must be lower than the humidity of 85%RH at 40°C.

### 5.3. Typical Operation Conditions

( Note 1 )

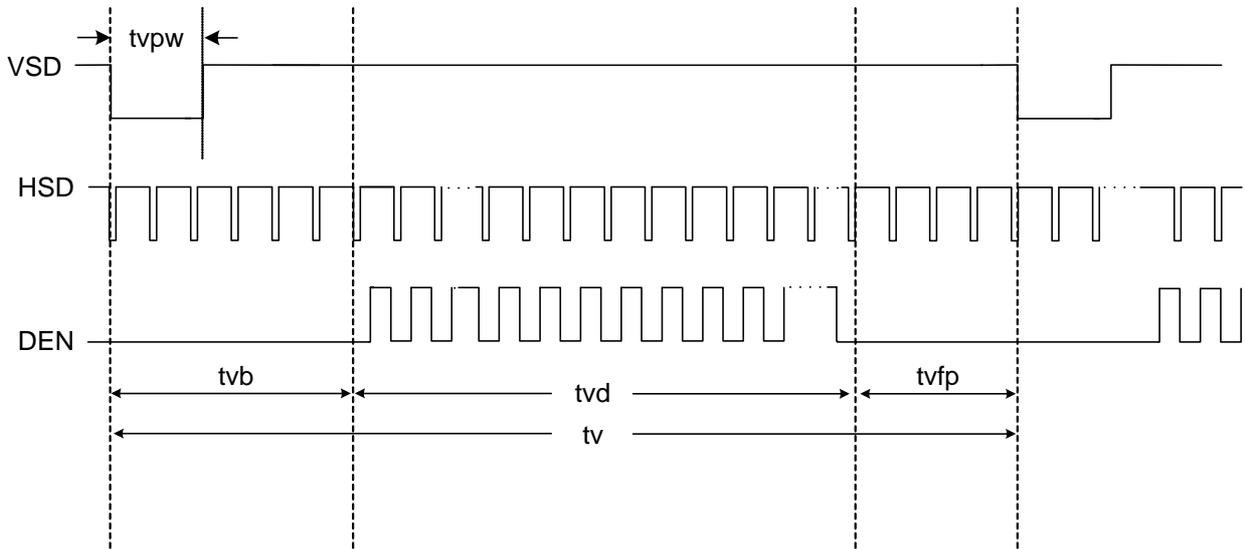
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	$DV_{DD}$	3.0	3.3	3.6	V	Note 2
	$AV_{DD}$	12	12.2	12.4	V	
	$V_{GH}$	19	22	25	V	
	$V_{GL}$	-13	-10	-7	V	
Input signal voltage	$V_{COM}$	4.4	(5.4)	6.4	V	
Input logic high voltage	$V_{IH}$	$0.7 DV_{DD}$	-	$DV_{DD}$	V	Note 3
Input logic low voltage	$V_{IL}$	0	-	$0.3 DV_{DD}$	V	

### 5.4 Power On-Off Sequence Timing

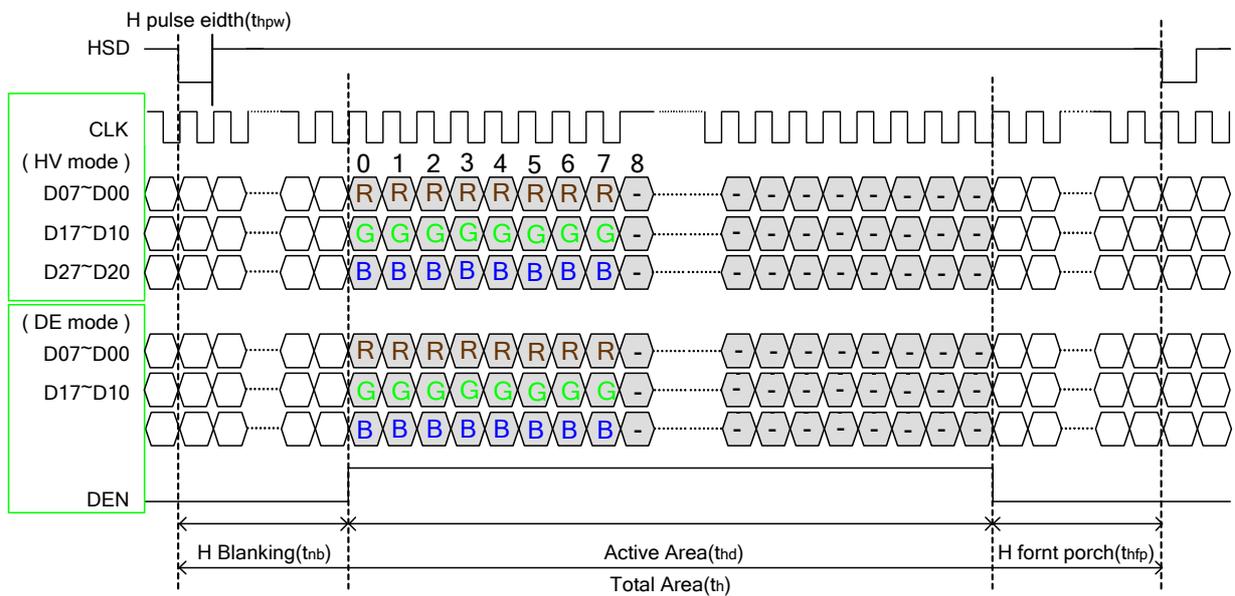


## 5.5 Data Input Format for TTL

### Vertical input timing



### Horizontal input timing



## 5.6. Timing Table

### DE Mode

Parameter	Symbol	Value			Unit
		Min	Typ.	Max	
DCLK frequency Frame rate = 60Hz	fclk	42.6	51.2	67.2	MHz
Horizontal display area	thd	1024			DCLK
HSYNC period time	th	1164	1344	1400	DCLK
HSYNC blanking	thb+thfp	140	320	376	DCLK
Vertical display area	tvd	600			H
VSYNC period time	tv	610	635	800	H
VSYNC blanking	tvb+tvfp	10	35	200	H

### HV Mode

#### a. Horizontal input timing

Parameter		Symbol	Value			Unit
Horizontal display area		thd	1024			DCLK
DCLK frequency Frame rate = 60Hz		fclk	Min	Typ.	Max	MHz
			44.9	51.2	63	
1 Horizontal Line		th	1200	1344	1400	DCLK
HSYNC pulse width	Min	thpw	1			DCLK
	Typ.		-			
	Max		140			
HSYNC blanking		thb	160	160	160	DCLK
HSYNC front porch		thfp	16	160	216	

#### b. Vertical input timing

Parameter	Symbol	Value			Unit
		Min	Typ.	Max	
Vertical display area	tvd	600			H
VSYNC period time	tv	624	635	750	H
VSYNC pulse width	tpw	1	-	20	H
VSYNC blanking	tvb	23	23	23	H
VSYNC front porch	tvfp	1	12	127	H

## 6. Electrical Specifications and Instruction Code

### 6.1 Electrical characteristics(V<sub>ss</sub>=0V ,T<sub>a</sub>=25°C)

Parameter	Symbol	Condition	Min	Typ	Max	Unit	Note	
Power supply	V <sub>CC</sub>	T <sub>a</sub> =25°C	2.6	3.3	3.6	V		
Input voltage	'H'	V <sub>IH</sub>	V <sub>CC</sub> =2.8V	0.8V <sub>CC</sub>	-	V <sub>CC</sub>	V	
	'L'	V <sub>IL</sub>	V <sub>CC</sub> =2.8V	0	-	0.2V <sub>CC</sub>	V	
Current Consumption	I <sub>CC1</sub>	Normal mode	-	-	-	mA	2	
	I <sub>CC2</sub>	Sleep mode	-	0.03	0.09	mA	2	

Note:

1:When an optimum contrast is obtained in transmissive mode.

2: Tested in 1×1 chessboard pattern.

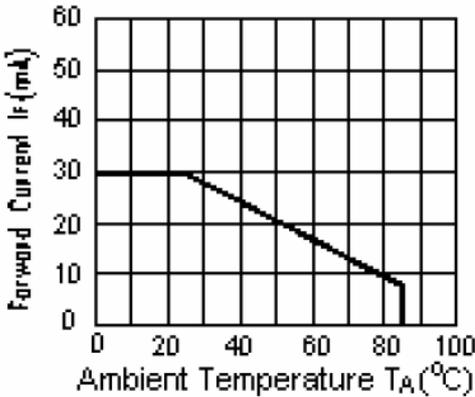
**6.2 LED backlight specification(VSS=0V ,Ta=25°C)**

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Supply voltage	-	-	9.0	9.9	10.5	V	1
Supply current	I <sub>f</sub>	-	-	220	-	mA	2

Note:

1:  $V_{LED} = V_{LED(+)} - V_{LED(-)}$ .

A LED drive in constant current mode is recommended.



CIRCUIT DIAGRAM

I<sub>f</sub>LED VS TEMP

### 6.3 Interface signals

Pin No.	Symbol	Function
1	V <sub>LED+</sub>	Power for LED backlight (Anode)
2	V <sub>LED+</sub>	Power for LED backlight (Anode)
3	V <sub>LED-</sub>	Power for LED backlight (Cathode)
4	V <sub>LED-</sub>	Power for LED backlight (Cathode)
5	GND	Ground
6	VCOM	Common voltage 3.3V
7	DVDD	Power for Digital Circuit
8	MODE	DE/SYNC mode select
9	DE	Data Enable Input
10	VSYNC	Vertical Sync Input
11	HSYNC	Horizontal Sync Input
12-19	B7-B0	Blue Data Bit
20-27	G7-G0	Green Data Bit
28-35	R7-R0	Red Data Bit / DX0-DX7
36	GND	Ground
37	DCLK	Dot Data Clock
38	GND	Ground
39	L/R	Left/Right selection
40	U/D	Up/Down selection
41	VGH	Gate ON Voltage 18V
42	VGL	Gate OFF Voltage -8.0V
43	AVDD	Power for Analog Circuit 10.3V
44	RESET	Reset pin, This is an active low signal
45	NC	NC
46	VCOM	Common voltage 3.3V
47	DITHB	Dithering function
48	GND	Ground
49-50	NC	NC

## 7. Optical Characteristics

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Note
Brightness	Bp	$\theta=0^\circ$	450	500	-	Cd/m <sup>2</sup>	1
Uniformity	$\Delta$ Bp	$\Phi=0^\circ$	75	80	-	%	1,2
Viewing Angle	3:00	Cr $\geq$ 10	-	80	-	Deg	3
	6:00		-	80	-		
	9:00		-	80	-		
	12:00		-	80	-		
Contrast Ratio	Cr	$\theta=0^\circ$ $\Phi=0^\circ$	-	1000		-	4
Response Time	T <sub>r</sub> +T <sub>f</sub>			25		ms	5
Color of CIE Coordinate	W	x	-	-	-	-	1,6
		y	-	-	-	-	
		Y	-	-	-	-	
	R	x	-	-	-	-	
		y	-	-	-	-	
		Y	-	-	-	-	
	G	x	-	-	-	-	
		y	-	-	-	-	
		Y	-	-	-	-	
	B	x	-	-	-	-	
		y	-	-	-	-	
		Y	-	-	-	-	
NTSC Ratio	S		50	-	%		

Note: The parameter is slightly changed by temperature, driving voltage and material

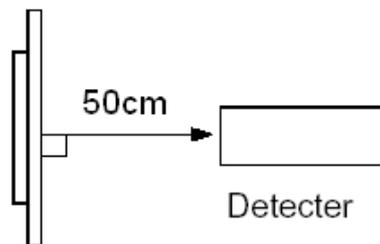
Note 1: The data are measured after LEDs are turned on for 5 minutes. LCM displays full white.

The brightness is the average value of 9 measured spots. Measurement equipment PR-705 ( $\Phi$ 8mm)

Measuring condition:

- Measuring surroundings: Dark room.
- Measuring temperature:  $T_a=25^{\circ}\text{C}$ .
- Adjust operating voltage to get optimum contrast at the center of the display.

Measured value at the center point of LCD panel after more than 5 minutes while backlight turning on.

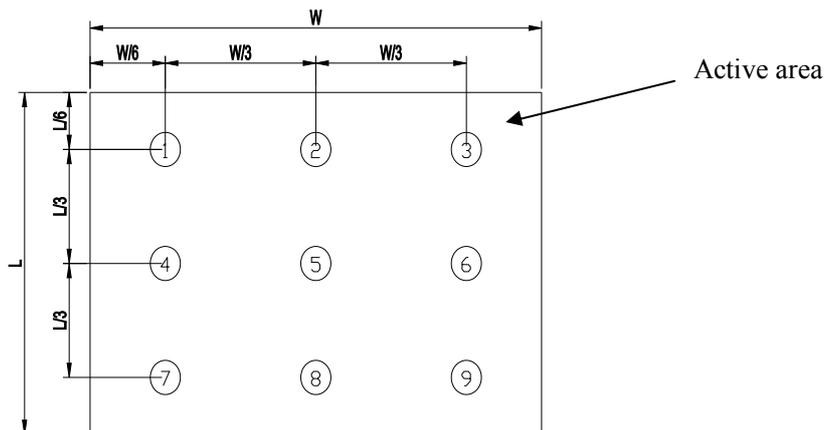


Note 2: The luminance uniformity is calculated by using following formula.

$$\Delta Bp = Bp (\text{Min.}) / Bp (\text{Max.}) \times 100 (\%)$$

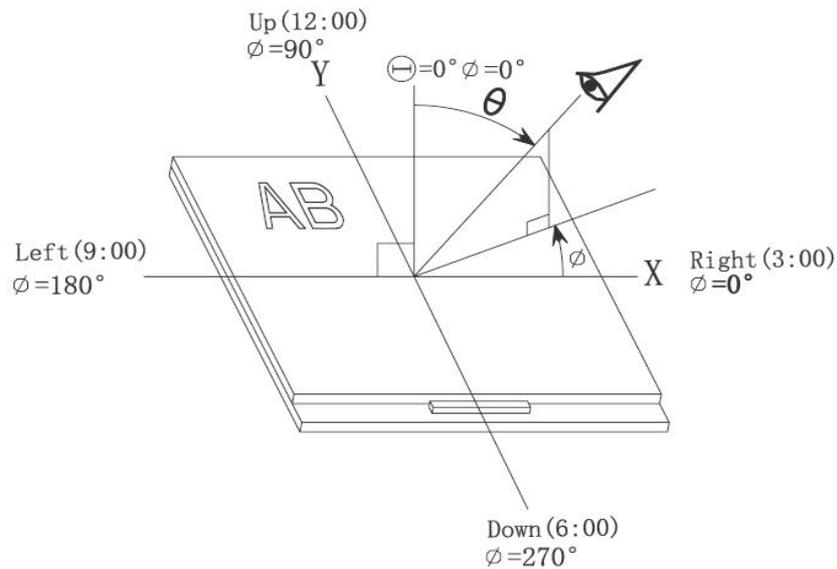
$Bp (\text{Max.})$  = Maximum brightness in 9 measured spots

$Bp (\text{Min.})$  = Minimum brightness in 9 measured spots.

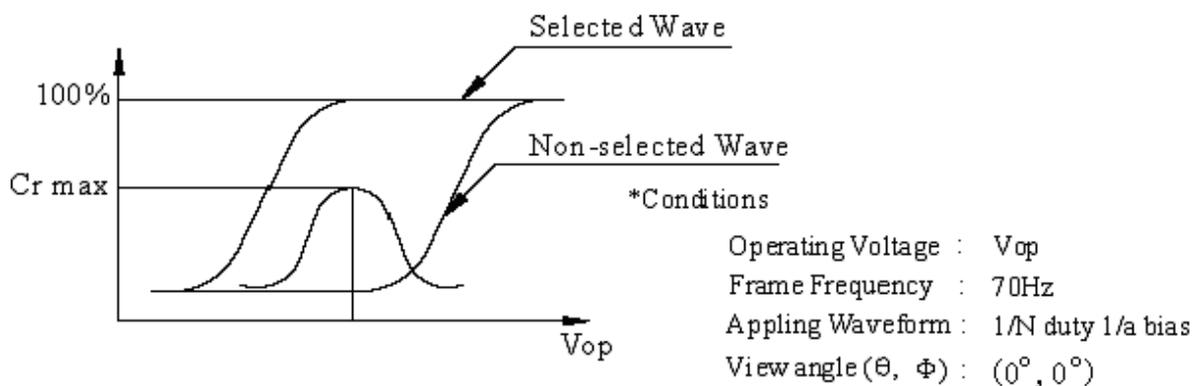


Note 3: The definition of viewing angle:

Refer to the graph below marked by  $\theta$  and  $\phi$



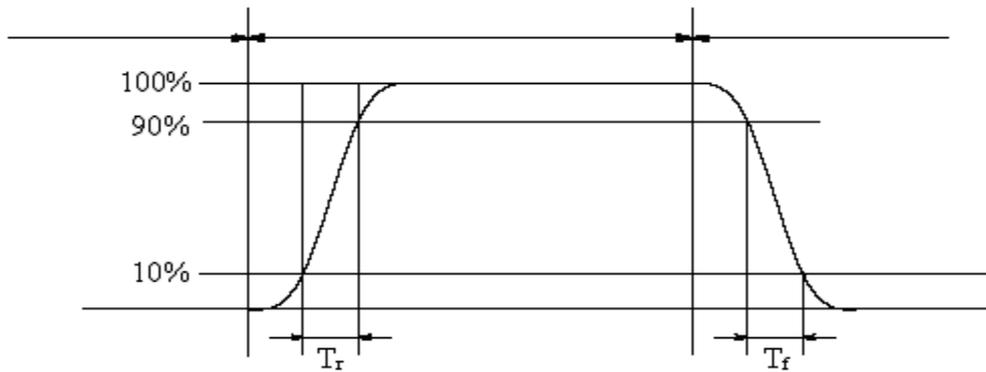
Note 4: Definition of contrast ratio.( Test LCD using DMS501)



$$\text{Contrast ratio}(Cr) = \frac{\text{Brightness of selected dots}}{\text{Brightness of non-selected dots}}$$

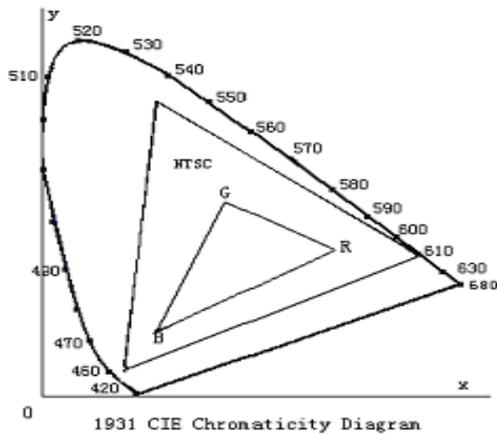
Note 5: Definition of Response time. (Test LCD using DMS501):

The output signals of photo detector are measured when the input signals are changed from “black” to “white”(falling time) and from “white” to “black”(rising time), respectively. The response time is defined as the time interval between the 10% and 90% of amplitudes.Refer to figure as below.



The definition of response time

Note 6: Definition of Color of CIE Coordinate and NTSC Ratio.

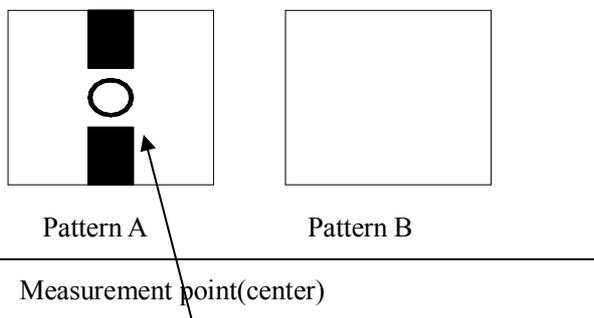


Color gamut:

$$S = \frac{\text{area of RGB triangle}}{\text{area of NTSC triangle}} \times 100\%$$

Note 7: Definition of cross talk.

$$\text{Cross talk ratio}(\%) = \frac{|\text{pattern A Brightness} - \text{pattern B Brightness}|}{\text{pattern A Brightness}} \times 100$$



Electric volume value=3F+/-3Hex

## 8. Reliability Test Items and Criteria

No	Test Item	Test condition	Criterion
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1	High Temperature Storage	80°C±2°C 96H Restore 2H at 25°C Power off	1. After testing, cosmetic and electrical defects should not happen. 2. Total current consumption should not be more than twice of initial value.
2	Low Temperature Storage	-30°C±2°C 96H Restore 2H at 25°C Power off	
3	High Temperature Operation	70°C±2°C 96H Restore 2H at 25°C Power on	
4	Low Temperature Operation	-20°C±2°C 96H Restore 4H at 25°C Power on	
5	High Temperature/Humidity Operation	60°C±2°C 90%RH 96H Power on	
6	Temperature Cycle	-20°C → 70°C 30min 5min 30min after 5 cycle, Restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s <sup>2</sup> , 120min	Not allowed cosmetic and electrical defects.
8	Shock Test	Half- sine wave, 300m/s <sup>2</sup> , 11ms	
9	ESD Test	Air discharge: +/-8KV, Contact discharge: 2KV	

Note: Operation: Supply 2.8V for logic system.

The inspection terms after reliability test, as below

ITEM	Inspection
Contrast	CR>50%
IDD	IDD<200%
Brightness	Brightness>60%
Color Tone	Color Tone+/-0,05

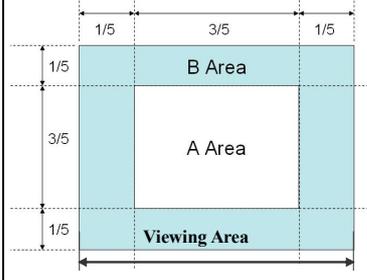
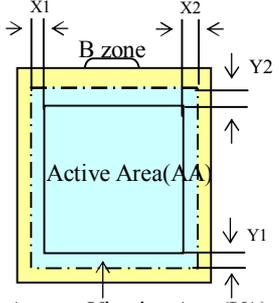
## 9 Quality level

### 9.1 Classification of defects

Major defects (MA): A major defect refers to a defect that may substantially degrade usability for product applications, including all functional defects (such as no display, abnormal display, open or missing segment, short circuit, missing component), outline dimension beyond the drawing, progressive defects and those affecting reliability.

Minor defects (MI): A minor defect refers to a defect which is not considered to be able to substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation, such as black spot, white spot, bright spot, pinhole, black line, white line, contrast variation, glass defect, polarizer defect, etc.

### 9.2 Definition of inspection range

<p>For dot defect of TFT LCD which is not smaller than 3 inches, dividing three areas to make a judgment (according to figure 1).</p> <p>A area : center of viewing area          B area : periphery of viewing area          C area : Outside viewing area</p> <p>For other defects, dividing two areas to make a judgment (according figure 2).</p> <p>A zone : Inside Viewing area          B zone : Outside Viewing area</p> <p>X1(A.A~V.A): 2mm    X2(A.A~V.A): 2mm          Y1(A.A~V.A): 2mm    Y2(A.A~V.A): 2mm</p>	 <p>Figure 1</p>  <p>Figure 2</p>
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### 9.3 Inspection items and general notes

<p>General notes</p>	<p>1.Should any defects which are not specified in this standard happen, additional standard shall be determined by mutual agreement between customer and TIANMA.</p> <p>2.Viewing area should be the area which TIANMA guarantees.</p> <p>3.Limit sample should be prior to this Inspection standard.</p> <p>4.Viewing judgment should be under static pattern.</p> <p>5.Inspection conditions          Inspection distance: 250 mm (from the sample)    Temperature : 25±5 °C          Inspection angle : 45 degrees in 6 o'clock direction (all defects in viewing area should be inspected from this direction)</p>	
<p>Inspection items</p>	<p>Pinhole, Bright spot, Black spot, White spot, Black line, White Line, Foreign particle, Bubble</p> <p>Contrast variation</p> <p>Polarizer defect</p> <p>Dot defect (TFT LCD)</p> <p>Functional defect</p> <p>Glass defect</p>	<p>The color of a small area is different from the remainder. The phenomenon doesn't change with voltage</p> <p>The color of a small area is different from the remainder. The phenomenon changes with voltage</p> <p>Scratch, Dirt, Particle, Bubble on polarizer or between polarizer and glass</p> <p>The pixel appears bright or dark abnormally when display</p> <p>No display, Abnormal display, Open or missing segment, Short circuit, False viewing direction</p> <p>Glass crack, Shaved corner of glass, Surplus glass</p>

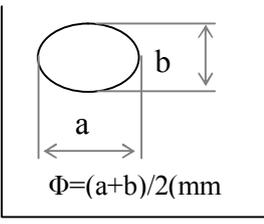
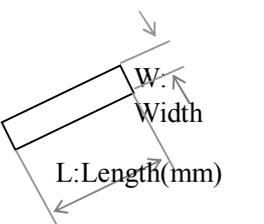
	PCB defect	Components assembly defect
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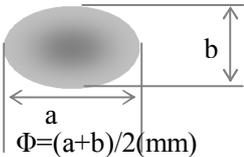
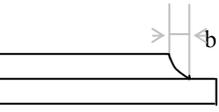
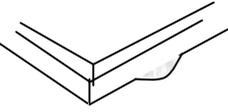
### 9.4 Outgoing Inspection level

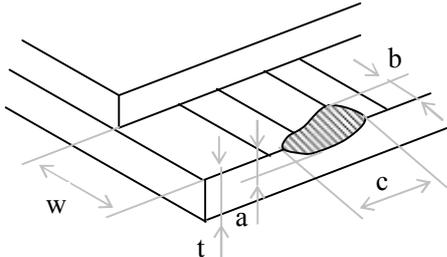
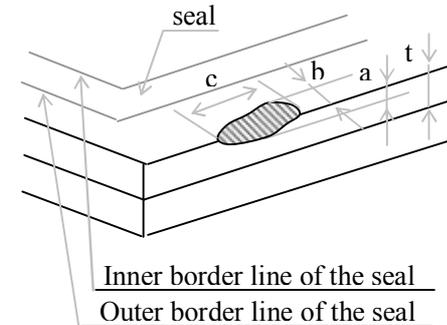
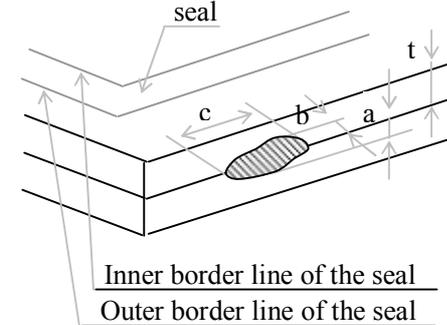
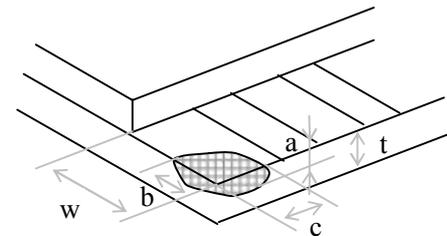
Outgoing Inspection standard	Inspection conditions	Inspection				
		Min.	Max.	Unit	IL	AQL
Major Defects	See 8.3 general notes	See 8.5			II	0.065
Minor Defects	See 8.3 general notes	See 8.5			II	0.065

Note: Sampling standard conforms to GB2828

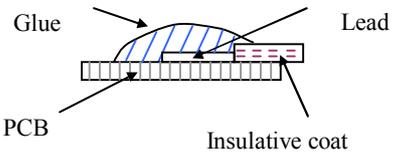
### 9.5 Inspection Items and Criteria

Inspection items			Judgment standard			
			Category		Acceptable number	
					A zone	B zone
1	Black spot, White spot, Pinhole, Foreign Particle, Particle in or on glass, Scratch on glass  $\Phi = (a+b)/2(\text{mm})$ $(a/b < 2.5)$	A	$\Phi \leq 0.20$		Neglected	Neglected
		B	$0.20 < \Phi \leq 0.25$		3	Neglected
		C	$0.25 < \Phi \leq 0.3$		2	Neglected
		D	$0.3 < \Phi \leq 0.4$		1	3
		E	$0.4 < \Phi \leq 0.5$		0	2
		Total defective point(B,C)		1	-	
2	Black line, White line, and Particle Between Polarizer and glass, Scratch on glass  $L/W \geq 2.5$	A	$W \leq 0.03$		Neglected	Neglected
		B	$0.03 < W \leq 0.05$ $L \leq 3.0$		3	Neglected
		C	$0.05 < W \leq 0.1$ $L \leq 3.0$		2	Neglected
		D	$0.05 < W \leq 0.1$ $L \leq 4.0$		1	3
		E	$W > 0.1$ $L > 4.0$		0	2
		Total defective point(B,C)		1	-	
3	Bright spot	any size		none	none	
4	Contrast	A	$\Phi < 0.2$		Neglected	Neglected

	variation		B	$0.2 < \Phi \leq 0.3$	2	
			C	$0.3 < \Phi \leq 0.4$	1	
			D	$0.4 < \Phi$	0	
			Total defective point(B,C)		3	
5	Bubble inside cell		any size		none	none
6	Polarizer defect (if Polarizer is used)	Scratch ,damage on polarizer, Particle on polarizer or between polarizer and glass.	Refer to item 1 and item 2.			
		Bubble, dent and convex	A	$\Phi \leq 0.1$	Neglected	Neglected
			B	$0.1 < \Phi \leq 0.2$	2	Neglected
			C	$0.2 < \Phi \leq 0.3$	1	2
7	Surplus glass	Stage surplus glass 	$B \leq 0.3\text{mm}$			
		Surrounding surplus glass 	Should not influence outline dimension and assembling.			
8	Open segment or open common		Not permitted			
9	Short circuit		Not permitted			
10	False viewing direction		Not permitted			
11	Contrast ratio uneven		According to the limit specimen			
12	Crosstalk		According to the limit specimen			
13	Black /White spot(display)		Refer to item 1			
14	Black /White line(display)		Refer to item 2			

Inspection items		Judgment standard		Acceptable number	
		Category(application: B zone)			
15	Glass defect crack	i ) The front of lead terminals 	A	$a \leq t, b \leq 1/5W, c \leq 3\text{mm}$	Max.3 defects allowed
			B	Crack at two sides of lead terminals should not cover patterns and alignment mark	
		ii ) Surrounding crack-non-contact side 		$b < \text{Inner borderline of the seal}$	
		iii ) Surrounding crack- contact side 		$b < \text{Outer borderline of the seal}$	
	iv ) Corner 	A	$a \leq t, b \leq 3.0, c \leq 3.0$		
		B	Glass crack should not cover patterns u and alignment mark and patterns.		

Inspection items		Judgment standard	
		Category(application: B zone)	
16	PCB defect	<p>Component soldering:            No cold soldering、short、open circuit、burr、tin ball            The flat encapsulation component position deviation must be less than 1/3 width of the pin (Pic.1);            the sheet component deviation:            Pin deviates from the pad and contact with the near components is not permitted (Pic.2)</p>	<p>Component</p> <p>Soldering pad Lead Component</p> <p><math>L \leq W/2</math></p> <p><math>W</math></p> <p><math>L1 &gt; 0</math></p> <p><math>L2 &gt; 0</math></p>
		<p>lead defect:            The lead lack must be less than 1/3 of its width;            The lead burr must be less than 1/3 of the seam;            Impurities connect with the near leads is not permitted</p>	
		<p>Connector soldering:            Soldering tin is at contact position of the plug and socket is not permitted            No foundation is scald            Serious cave distortion on plug and socket contact pin is not permitted</p>	<p>head Base Board</p> <p>Soldering tin is not permit in this area</p> <p>Soldering tin is not permit in this area</p> <p>socket Base Board</p>

	<p>Glue on root of the speaker receiver and motor lead: The insulative coat of the lead must join into the PCB; the protected glue must envelop to the insulative coat.</p>	
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## 10. Precautions for Use of LCD Modules

### 10.1 Handling Precautions

10.1.1 The display panel is made of glass. Do not subject it to a mechanical shock by dropping it from a high place, etc.

10.1.2 If the display panel is damaged and the liquid crystal substance inside it leaks out, be sure not to get any in your mouth, if the substance comes into contact with your skin or clothes, promptly wash it off using soap and water.

10.1.3 Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.

10.1.4 The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.

10.1.5 If the display surface is contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If still not completely clear, moisten cloth with one of the following solvents:

- Isopropyl alcohol
- Ethyl alcohol

Solvents other than those mentioned above may damage the polarizer.

Especially, do not use the following:

- Water
- Ketone
- Aromatic solvents

10.1.6 Do not attempt to disassemble the LCD Module.

10.1.7 If the logic circuit power is off, do not apply the input signals.

10.1.8 To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.

a. Be sure to ground the body when handling the LCD Modules.

b. Tools required for assembly, such as soldering irons, must be properly ground.

c. To reduce the amount of static electricity generated, do not conduct assembly and other work under dry conditions.

d. The LCD Module is coated with a film to protect the display surface. Be care when peeling off this protective film since static electricity may be generated.

## **10.2 Storage precautions**

10.2.1 When storing the LCD modules, avoid exposure to direct sunlight or to the light of fluorescent lamps.

10.2.2 The LCD modules should be stored under the storage temperature range.

If the LCD modules will be stored for a long time, the recommend condition is:

Temperature :         $0^{\circ}\text{C} \sim 40^{\circ}\text{C}$

Relatively humidity:  $\leq 80\%$

10.2.3 The LCD modules should be stored in the room without acid, alkali and harmful gas.

**10.3 The LCD modules should be no falling and violent shocking during transportation, and also should avoid excessive press, water, damp and sunshine.**